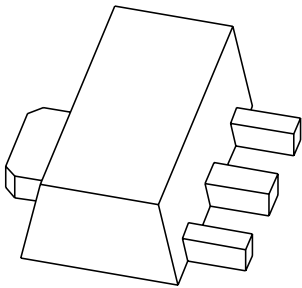


DATA SHEET



BCV28; BCV48 PNP Darlington transistors

Product specification
Supersedes data of 1999 Apr 08

2004 Dec 06

PNP Darlington transistors

BCV28; BCV48

FEATURES

- Very high DC current gain (min. 10000)
- High current (max. 500 mA)
- Low voltage (max. 60 V).

APPLICATIONS

- Where very high amplification is required.

DESCRIPTION

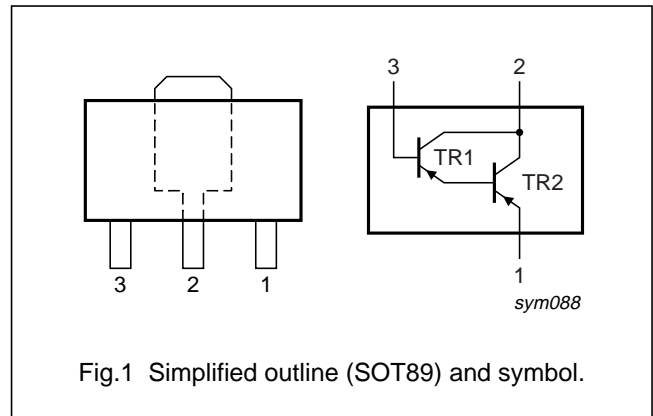
PNP Darlington transistor in a SOT89 plastic package.
NPN complements: BCV29 and BCV49.

MARKING

TYPE NUMBER	MARKING CODE
BCV28	ED
BCV48	EE

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
BCV28	SC-62	plastic surface mounted package; collector pad for good heat transfer; 3 leads	SOT89
BCV48			

PNP Darlington transistors

BCV28; BCV48

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BCV28		–	–40	V
	BCV48		–	–80	V
V _{CES}	collector-emitter voltage	V _{BE} = 0 V			
	BCV28		–	–30	V
	BCV48		–	–60	V
V _{EBO}	emitter-base voltage	open collector	–	–10	V
I _C	collector current (DC)		–	–500	mA
I _{CM}	peak collector current		–	–800	mA
I _B	base current (DC)		–	–100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	–	1.3	W
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	ambient temperature		–65	+150	°C

Note

- Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm².
For other mounting conditions, see *“Thermal considerations for SOT89 in the General Part of associated Handbook”*.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	96	K/W
R _{th(j-s)}	thermal resistance from junction to soldering point		16	K/W

Note

- Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm².
For other mounting conditions, see *“Thermal considerations for SOT89 in the General Part of associated Handbook”*.

PNP Darlington transistors

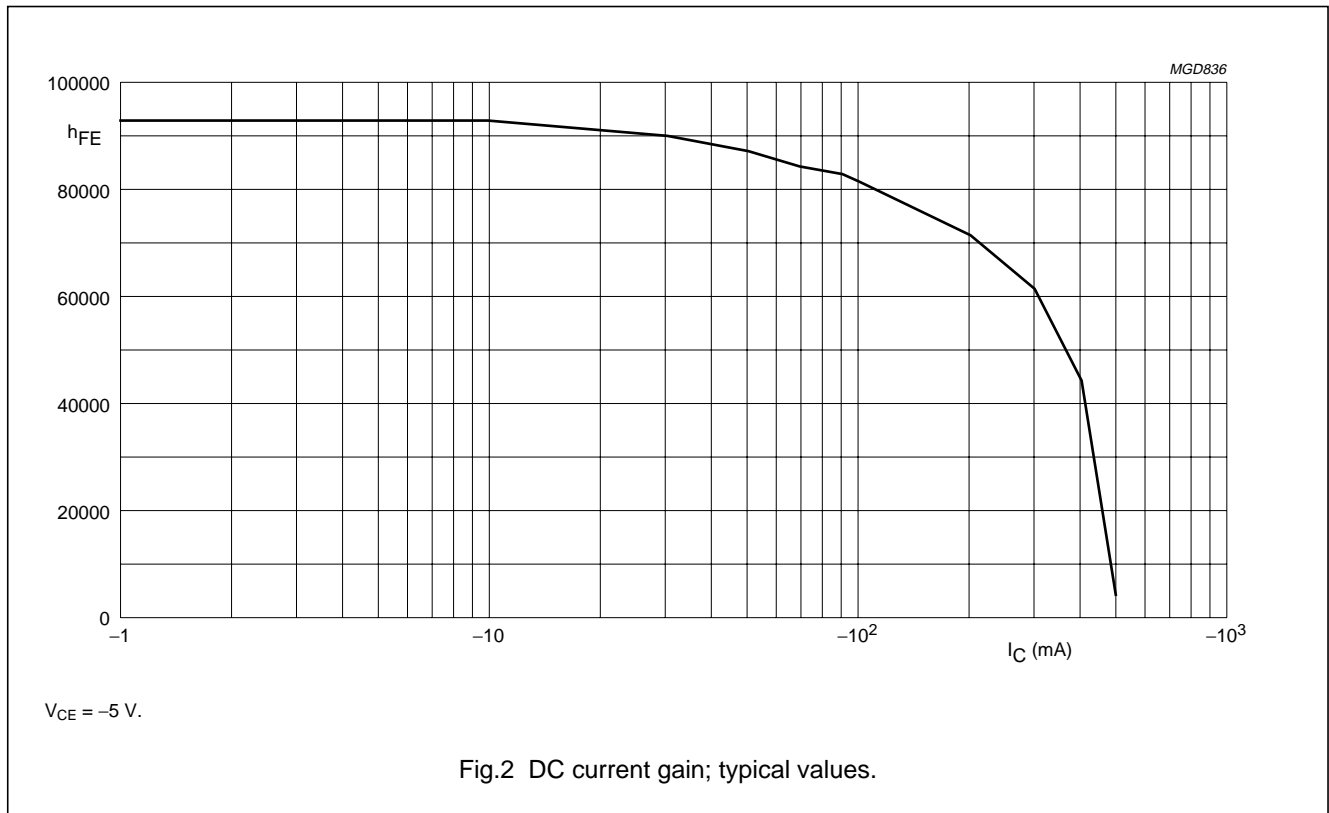
BCV28; BCV48

CHARACTERISTICST_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current					
	BCV28	I _E = 0 A; V _{CB} = -30 V	-	-	-100	nA
	BCV48	I _E = 0 A; V _{CB} = -60 V	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	I _C = 0 A; V _{BE} = -10 V	-	-	-100	nA
h _{FE}	DC current gain	I _C = -1 mA; V _{CE} = -5 V; see Fig.2				
	BCV28		4000	-	-	
	BCV48		2000	-	-	
	DC current gain	I _C = -10 mA; V _{CE} = -5 V; see Fig.2				
	BCV28		10000	-	-	
	BCV48		4000	-	-	
	DC current gain	I _C = -100 mA; V _{CE} = -5 V; see Fig.2				
	BCV28		20000	-	-	
BCV48		10000	-	-		
V _{CEsat}	collector-emitter saturation voltage	I _C = -100 mA; I _B = -0.1 mA	-	-	-1	V
V _{BEsat}	base-emitter saturation voltage	I _C = -100 mA; I _B = -0.1 mA	-	-	-1.5	V
V _{BEon}	base-emitter on-state voltage	I _C = -10 mA; I _B = -5 mA	-	-	-1.4	V
f _T	transition frequency	I _C = -30 mA; V _{CE} = -5 V; f = 100 MHz	-	220	-	MHz

PNP Darlington transistors

BCV28; BCV48



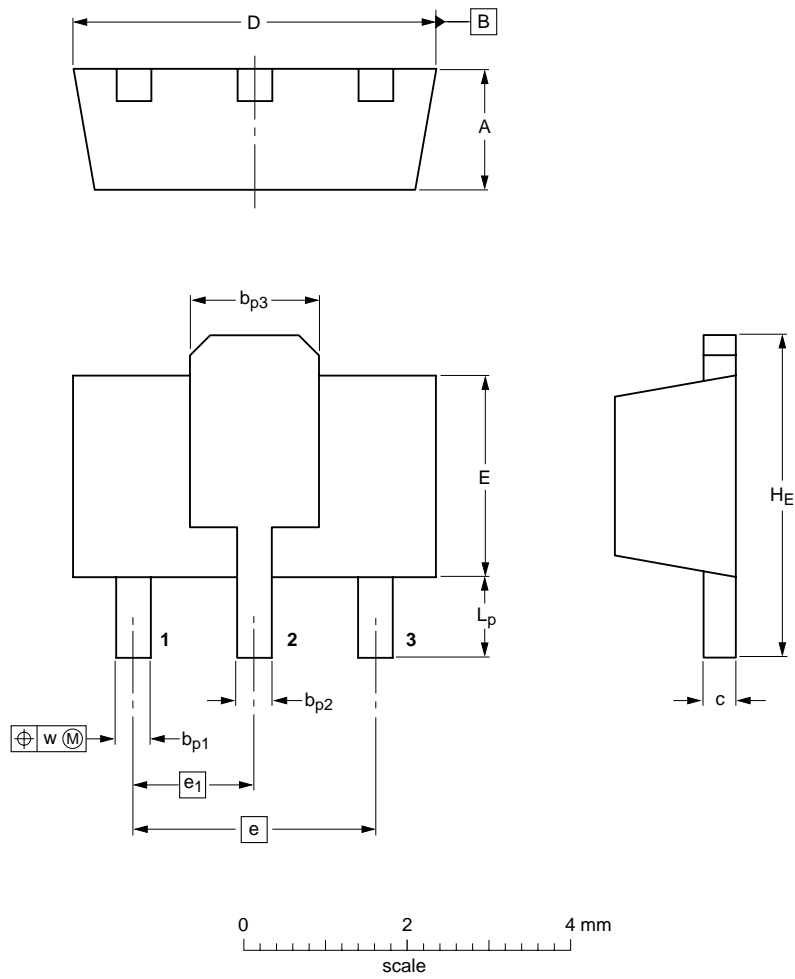
PNP Darlington transistors

BCV28; BCV48

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



DIMENSIONS (mm are the original dimensions)

UNIT	A	b_{p1}	b_{p2}	b_{p3}	c	D	E	e	e_1	H_E	L_p	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.23	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	1.2 0.8	0.13

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA		
SOT89		TO-243	SC-62		99-09-13 04-08-03

PNP Darlington transistors

BCV28; BCV48

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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SCA76

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